supplying the clock and video data signals associated with rectangular subareas of the two-dimensional rectangular raster image to each visual display unit, and a sum of character areas or pixels which can be driven separately on all the visual display units being less than or equal to a number of clock and video data signals which are emitted by the screen control unit

operating the visual display units in the single scan mode from a screen control unit which is set to dual scan mode.

2.The method as claimed in claim **1**, wherein a sum of the characters and pixels which can be displayed on all the visual display units is greater than a number of video data signals which are emitted by the screen control unit, and further comprising:

supplying at least one of clock and video data signals to at least a part of a subarea of more than one visual display unit.

3. A circuit arrangement for for independent operation of two visual display units which are operated in the single scan mode, and which each have a data input and a clock signal input and one of which is intended for displaying an upper subarea of a frame, and the other of which is intended for displaying a lower subarea of a frame, comprising:

screen control unit which is set up for dual scan mode and has an upper data bus and a lower data bus and a control bus, and

the clock signal inputs of both of said visual display units being connected in parallel to the control bus of the screen control unit, the data input of one of said two visual display units being connected to the upper data bus, and the data input of an other of said two visual display units being connected to the lower data bus of the screen control unit.

4.A circuit arrangement for independent operation of two visual display units which are operated in the single scan mode and have a data input and a clock signal input, and one of which is intended for displaying a left-hand subarea of a frame and an other of which is intended for displaying a right-hand subarea of a frame, comprising:

screen control unit which is set up for dual scan mode and has an upper data bus and a lower data bus and a control bus,

the clock signal input of a first of said two visual display units which is displaying one subarea being connected directly to said control bus of the screen control unit,

a counting gate circuit,

the clock signal input of a second of said two visual display units which is displaying an other subarea being connected via said counting gate circuit to the control bus of the screen control unit, and

the data inputs of the two visual display units being connected in parallel to said upper and lower data buss of the screen control unit.

5. A method as claimed in claim **1**, wherein said dual scan mode is a VGA dual scan mode of a VGA controller.

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